

Notice of Allowability

Application No.

10/600,757

Examiner

Phu Vu

Applicant(s)

KIM ET AL.

Art Unit

2871

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 5/22/06.
2. ☒ The allowed claim(s) is/are 1-6,9-24,26-32 and 34.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

Allowable Subject Matter

Claims 1-6, 9-24, 26-32 and 34 allowed.

The following is an examiner's statement of reasons for allowance:

Regarding claims 1-6, 9-24, 26-32, and 34, the prior art fails to teach in solely or in combination a static electricity prevention circuit comprising: a picture display part having a plurality of liquid crystal cells; a signal pad part for applying a driving signal to be inputted to signal lines of the picture display part, wherein the signal pad part includes: a plurality of pads connected to respective ones of the signal lines; and a static electricity prevention circuit having a TFT with a floating gate for connecting at least one of the said pads with an equipotential line in the presence of static electricity; and a link line connecting the one of said pads to the TFT transistor respectively, **wherein the link line is connected to a node between an outer edge of the picture display part and the TFT, and to an outer edge of the picture display part and the TFT, and to an outer edge of one of the pads near the equipotential line.**

Wu teaches a picture display part having a plurality of liquid crystal cells; a signal pad part for applying a driving signal to be inputted to signal lines of the picture display part, wherein the signal pad part includes: a plurality of pads connected to respective ones of the signal lines; and a static electricity prevention circuit having a TFT with a floating gate for connecting at least one of the said pads with an equipotential line in the presence of static electricity; and a link line connecting the one of said pads to the TFT transistor respectively, however fails to teach a link line connected to a node between

the outer edge of the picture display part and TFT, and to an outer edge of one of the pads to the equipotential line. Wu teaches a link line connecting TFT (element 50 see fig. 10) to the pad however it is not connect to a node between an outer edge of the picture display part and TFT as there is no node formed between the picture display part and the TFT. Alternatively interpreting "between the picture display part and the TFT" as a purely spatial limitation than it meets the limitation of being formed between the picture display element and TFT however there is no "node" that the link line is connected to as a node refers to "a connecting point in which several lines come together" (definition provided by Princeton Wordnet). The reference further supports both of these interpretations of this limitation as fig. 6 clearly shows link line 66 forming a node between pad (62), FTFT (69), and another element which is another static prevention circuit (68) or a resistor element shown in other figures.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."


Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phu Vu whose telephone number is (571)-272-1562. The examiner can normally be reached on 8AM-5PM M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571)-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Phu Vu
Examiner
AU 2871


ANDREW SCHECHTER
PRIMARY EXAMINER